## UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,023,755 B2

Page 1 of 1

APPLICATION NO.: 10/725315 : April 4, 2006

INVENTOR(S)

: Scott Van De Graaff et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 55, "OS" should reads -- CMOS--;

Column 10, line 50, "coupled the" should read --coupled to the--;

Column 11, line 18, "also coupled" should read -- also be coupled--;

Column 11, line 28, "an local" should read --a local--;

Column 11, line 32, "21" should read --916--;

Column 11, line 38, "221" should read --921--; and

Column 11, line 54 "system" should read --systems--.

Signed and Sealed this

Tenth Day of October, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office